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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/566,031	01/18/2006	Hideo Hara	40404.19/mo	9352
54068 ROHM CO., L.	7590 09/29/200 ΓD.	8	EXAM	IINER
C/O KEATING & BENNETT, LLP 1800 Alexander Bell Drive			LEE, MICHAEL	
SUITE 200	r Bell Drive		ART UNIT	PAPER NUMBER
Reston, VA 201	191		2622	
			NOTIFICATION DATE	DELIVERY MODE
			09/29/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

JKEATING@KBIPLAW.COM uspto@kbiplaw.com

	Application No.	Applicant(s)	
	10/566,031	HARA ET AL.	
Office Action Summary	Examiner	Art Unit	
	M. Lee	2622	
The MAILING DATE of this communication a Period for Reply	ppears on the cover sheet wit	h the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REF WHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNIC 1.136(a). In no event, however, may a re od will apply and will expire SIX (6) MON ute, cause the application to become AB	ATION. ply be timely filed THS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on 18 This action is FINAL . 2b) ☑ The 3) ☐ Since this application is in condition for allow closed in accordance with the practice under	nis action is non-final. vance except for formal matte	· •	
Disposition of Claims			
4) Claim(s) 1-8 is/are pending in the application 4a) Of the above claim(s) is/are withdi 5) Claim(s) is/are allowed. 6) Claim(s) 1-4 and 8 is/are rejected. 7) Claim(s) 5-7 is/are objected to. 8) Claim(s) are subject to restriction and Application Papers 9) The specification is objected to by the Examination of the drawing(s) filed on is/are: a) are applicant may not request that any objection to the	rawn from consideration. /or election requirement. ner. ccepted or b) □ objected to b	•	
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the	ection is required if the drawing(s) is objected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a li	nts have been received. Ints have been received in Apiority documents have been eau (PCT Rule 17.2(a)).	oplication No received in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	Paper No(s	ummary (PTO-413) /Mail Date formal Patent Application 	

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-4 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tanaka (4,319,275).

Regarding claim 1, Tanaka discloses a sync separation system showing a low-pass filter (10) and a comparator 12 for separating a synchronization signal, which meet the synchronization unit as claimed, and a microcomputer unit 14 for comparing a memory content with a predetermined reference value triggered by a FLAG 16 signal and generating a VSYNC detection signal (col. 7, lines 31-64). The reference value is adjustable by the microcomputer (note col. 6, lines 21-67). However, Tanaka does not disclose that the cutoff frequency of the low-pass filter is adjustable. In any event, the examiner takes Official Notice that using cutoff frequency adjustable low-pass filter, such as a RC circuit with a variable resistor or a capacitor, to filter a signal is well known in the art because it enables the low passing frequency spectrum be adjusted conveniently in order to compensate circuit parameter fluctuations. For instance, one of the well known parameters is temperature. Temperature can change the frequency response of the filter and thus cause inaccurate filtering. Hence, it would have been obvious to one of ordinary skill in the art at the time that the invention was made to

modify Tanaka so that the cutoff frequency of the low-pass filter could be adjusted in order to compensate circuit parameter fluctuations.

Regarding claims 2 and 3, in addition of above, it would also have been obvious to include a logic circuit to output the cutoff frequency adjustment to the low-pass filter from the microcomputer 14 in order to further enhance the operability of the system.

Regarding claim 4, see Official Notice taken above.

Regarding claim 8, although not shown, the microcomputer 14 is hinting to be integrated into a semiconductor IC chip since an IC chip consumes less energy than its discrete component counterpart. Hence, it would have been obvious to one of ordinary skill in the art at the time that the invention was made to integrate the logic circuit and the microcomputer 14 into a single semiconductor IC chip so that the energy consumption rate could be reduced.

Allowable Subject Matter

3. Claims 5-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Hiraguri (4,158,857) shows a mono-multi vibrator.

White (6,108,043) shows a low-pass filter.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to M. Lee whose telephone number 571-272-7349. The examiner can normally be reached on Monday through Thursday from 9 to 6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sinh Tran, can be reached on 571-272-7564. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/M. Lee/ Primary Examiner Art Unit 2622